**Simple and Generic Tone Generator for IP Phone**

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**ABSTRACT**

IP Phones need to handle Call Progress Tones (CPT) generation to provide an audible indication of the state of the call similar to the PSTN phone. Additionally, in case of systems having a keypad interface, DTMF tones need to be generated based on the key activated, either during call setup or during mid-call session. So, very low complexity generic Tone generator (TG) is required for any application, which needs to generate tones. The digital oscillator algorithm is used for generation of a single sinusoidal wave frequency. Both amplitude and frequency modulated tones are generated by different sine or cosine frequency separately mixed as per requirement of modulation scheme used. This algorithm is tested with a large number of other signaling tones (such as MF-R1, MF-R2, Caller ID Alerting Signal (CAS) etc.), and the results establish that the method employed is a good compromise in terms of performance, memory requirements, and precision. Design guidelines and implementation details of generic TG are presented to further emphasize the mathematical effectiveness of the method employed.

**Keywords**

Call Progress Tones, Tone Parameters, Oscillator co-efficients, DTMF tones.

**1 INTRODUCTION**

Multiple developments have taken place in packet based communication systems such as IP phones. To provide an audible indication of the state of the call similar to PSTN call in the IP phones, the Call Progress Tones (CPT) such as, dial tone, busy tone, ring tone, ring-back tone etc. are to be generated during call setup process. Besides, in case of systems having a keypad interface, Dual tone Multi Frequency (DTMF) tones are to be generated based on the key activated, either during call setup or during mid-call session. In either case, the DTMF [1] tone is generated locally, based on the key activated. During mid-call, in addition to local tone generation, the activated key information is passed to the other party as per RFC2833 [2].

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In this paper, design of a simple and generic tone generator is presented with test results. This generic tone generator can generate tones with configurable tone parameters. Also, the design provides low MIPS consumption (approx. 0.5 MIPS) on ARM9E core. As mentioned, additional tones or country specific tones can be easily added or modified by just adding or modifying the required parameters in the tone table which holds tone parameters for different tones with their IDs (denoted by tone_id).

**2 THEORY OF DIGITAL OSCILLATOR**

The digital oscillator algorithm is used for generation of a single sinusoidal wave. A Tone, which has more than one frequency at a time is resolved into different sine or cosine components and is generated separately using different oscillators. All tones generated from the different oscillators are mixed for the resultant wave. Generation of a single tone basically implies generating samples of a sine/cosine wave. The Z-transform of a sine wave is given in equation (1).

$$Z(\sin \theta) \frac{Y(z)}{X(z)} = \frac{z \sin \theta}{z^2 - 2z \cos \theta + 1}$$  \hspace{1cm} (1)

The impulse response of the above transform in equation (1) will generate a sine wave of frequency $\theta$. Thus the above equation is translated to equation (2) as below.

$$Y(z) = \frac{z^{-1} \sin \theta}{1 - 2z^{-1} \cos \theta + z^{-2}}$$  \hspace{1cm} (2)

Equation (2) can be rewritten in a difference equation form as follows:

$$y(n) - 2y(n-1) \cos \theta + y(n-2) = x(n-1) \sin \theta$$  \hspace{1cm} (3)

Thus, the equation for the oscillator is given as follows:

$$x(n) = 2 \cos(2\pi f / f_s) x(n-1) - x(n-2)$$  \hspace{1cm} (4)
where, \( x(n) \) is a sine wave signal, \( f \) is the required frequency, \( f_s \) is the sampling frequency and \( 2 \cos(2\pi f / f_s) \) is the resonator constant or oscillator co-efficient. The amplitude and phase of the sine wave are determined by the initial states \( x(-2) \) and \( x(-1) \). The amplitude \( A \) of the tone is estimated from the power level required as per equation (5). The initial conditions for generating a sine wave are given in the equation (6) and for the cosine wave generation are given in the equation (7).

\[
A = 10^{\text{Level(dBm0)/20}}
\]  (5)

\[
x(-2) = \pm A \sin(2\pi f / f_s), \ x(-1) = 0
\]  (6)

\[
x(-2) = \pm A, \ x(-1) = \pm A \cos(2\pi f / f_s)
\]  (7)

For a pre-defined number of target frequencies, the initialization values can be pre-computed. However, a generic tone generation requires the computation of the initial conditions with high precision for any frequency. To compute oscillator co-efficient polynomial approximation is used. This method uses the Taylor’s expansion of a cosine given as:

\[
\cos x = \sum_{k=0} a_k x^{2k}, \ a_k = (-1)^k / ((2k)!) \]  (8)

Replacing \( x \) with the \( 2\pi f / f_s \) and defining the normalized frequency as follows yields \( f_n = f / f_s \)

\[
\cos(2\pi f_n) = \sum_{k=0} b_k f_n^{2k}, \ b_k = (2\pi)^{2k} a_k
\]  (9)

The actual implementation uses a factorized version of this formula with a finite number of elements and normalized coefficients. A sixth-order polynomial provides sufficient precision for 16-bit coefficients; furthermore, the same polynomial can be used to compute either sine or cosine. Thus, the final polynomial approximation is given in the equation (10) and (11).

\[
\cos(2\pi f_n) = 2^m ((c_1 f_n^2 + c_2) f_n^2 + c_3) f_n^2 + c_4
\]  (10)

\[
c_i = 2^m b_i
\]  (11)

The corresponding normalized coefficients to be stored in memory are scaled by \( 2^{-7} \) (that is, \( m = 7 \)) and listed in following Table 1.

<table>
<thead>
<tr>
<th>Co-efficient</th>
<th>Values</th>
</tr>
</thead>
<tbody>
<tr>
<td>( c_0 )</td>
<td>0.0078125</td>
</tr>
<tr>
<td>( c_1 )</td>
<td>-0.1542053</td>
</tr>
<tr>
<td>( c_2 )</td>
<td>0.5073242</td>
</tr>
<tr>
<td>( c_3 )</td>
<td>-0.6676025</td>
</tr>
</tbody>
</table>

### 3 SYSTEM ARCHITECTURE

In the design of tone generation process, tone to be generated is decomposed into elementary components. So, each tone cycle consists of concatenated multiple tone components with or without silence and each component has its own tone parameters. The maximum number of components allowed could be limited based on the different country specifications. A maximum of four components are required in a tone cycle and two frequencies are required in a component to support most of the specifications of different countries in the world [3]. The tone cycle can be repeated as often as needed to form the signaling tone.

Each component is defined by the following parameters: (i) **Control Register**: Defines whether the two sinusoids are to be added or modulated, the initial phase (0 or 180), and the frequency unit (\( f \) or \( 3/f \)). This flag is stored in a byte. The meaning of each bit in the control register is given in Table 2, (ii), **Component cycles**: This specifies the number of times the tone component (ON + OFF) in a tone cycle must be generated to complete the component, (iii) **ON duration**: Indicates the duration of the tone ON (in ms), (iv) **OFF duration**: Indicates the duration of the silence period (in ms) that follows the ON period, (v) **Frequencies** (in Hz) and (vi) **Levels** (in dBm0).

The high level architecture of a tone component generation is shown in Figure 1. The Tone Parameter Extraction block will extract the tone parameters (frequencies, cadence, modulation, phase and levels) from the tone tables, corresponding to the ‘tone id’ of the tone to be generated. The Tone Component Controller block decrement the component count to be generated by 1 whenever ON and OFF samples to be generated is equal to 0. If number of components to be generated is equal to 0, then Tone Cycle Controller checks whether any more cycles need to be generated. But, if component count is greater than 0 then Parameter Estimation & Setting blocks will initialize the next tone component to be generated.
The following parameters are initialized for the generation of a tone component: (i) generate_cycles is initialized with number of cycles to be generated, (ii) component_count is initialized with number of tone components in the tone cycle, (iii) repeat_comp, which describe number of times tone component need to be repeated in a tone cycle, is initialized with number of times the current component in generation need to be repeated for completing the component (iv) Resonator constants (hereafter called as oscillator co-efficients) are estimated for the each frequency of the tone component and are initialized, (v) ON and OFF samples to be generated are initialized with converting ON and OFF duration of tone component into number of samples (vi) Amplitude is initialized based on the power level of the frequency. The amplitude conversion is done from dBm0 value as per equation (5), (vii) Modulation scheme, waveform type (sin or cos) and dual tone generation are initialized based on values of appropriate bits in the control register and (viii) Oscillator initial memory parameter \( x(n-2) \) and \( x(n-1) \) are initialize based on phase angle of each frequency.

Tone Component Generation block calls respective tone generation algorithm depending on number of frequencies in a tone component for generating tone samples during ON time and fills zero for generating silence during OFF time. Component Cycle Control block decrement the repeat_comp by 1 and signal Parameter Estimation & Setting block to initialize the state for present tone component for repeating if the repeat_comp is greater than 0 with ON and OFF samples to be generated is 0. If the repeat_comp and also ON and OFF samples to be generated are equal to 0, the Tone Cycle Control block decrement the component count. If number of components to be generated is equal to 0, then Tone Cycle Controller decrement generate_cycles and checks whether any more cycles need to be generated. If generate_cycles is greater than 0, it signals Parameter Estimation & Setting block to initialize the state with first parameters of tone component.

### Table 2. Description of Control Register

<table>
<thead>
<tr>
<th>Bit position</th>
<th>Bit description</th>
<th>Value</th>
<th>Description for the chosen value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Bit 0-2</td>
<td>Modulation type</td>
<td>0</td>
<td>No modulation, addition for dual frequency tone or single tone only</td>
</tr>
<tr>
<td>Bit 3</td>
<td>Phase 1</td>
<td>0</td>
<td>Frequency1 initialized with SINE wave</td>
</tr>
<tr>
<td>Bit 4</td>
<td>Phase 2</td>
<td>0</td>
<td>Frequency2 initialized with SINE wave</td>
</tr>
<tr>
<td>Bit 5</td>
<td>Unit 1</td>
<td>0</td>
<td>Frequency1 initialized with COS wave</td>
</tr>
<tr>
<td>Bit 6</td>
<td>Unit 2</td>
<td>0</td>
<td>Frequency2 initialized with COS wave</td>
</tr>
<tr>
<td>Bit 7</td>
<td>Reserved</td>
<td>0</td>
<td>Frequency2 initialized with frequency division</td>
</tr>
</tbody>
</table>

### Figure 1. Top-level architecture of generic TG

**4 PERFORMANCE EVALUATION**

The performance of the algorithm is evaluated by performing the following tests: (i) Frequency accuracy test, (ii) Power level accuracy tests, (iii) ON-OFF time tests, (iv) RFC events generation tests and (v) RFC tone generation tests.

A Hanning-windowed Fast Fourier Transform (FFT) is used to measure the output frequency. The typical frequency accuracy requirement is about ±1% of the nominal frequency. For an 8 kHz sampling rate, at least 4096 samples (512 ms) are required to ensure a good frequency resolution. The valid range of power...
level can be 0 dBm0 to –63 dBm0. If power level is 63 (representing –63 dBm0), the filter states are initialized with zeroes so that no tone samples are generated for this frequency. If a single frequency is generated with the modulation flag set, the resulting signal will be silence. For a continuous tone the component is repeated with a longest possible duration for performance reasons. For tone consisting of two or more frequencies, the effective level will not be equal to a simple addition of the two levels. If N sources generating the same sound level, are combined, the overall Sound Pressure Level (SPL) will increase by 10 log N dBm0. When one tone level in a combination is greater that the others, the resultant can be estimated from the following Table 3.

The frequency accuracy of the algorithms designed is shown in Figure 2 & 3. The worst-case frequency precision observed is 0.3%. The test results confirm the high accuracy of the algorithm design.

<table>
<thead>
<tr>
<th>Difference in the levels (dBm0)</th>
<th>Add to Higher Level (dBm0)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 – 1</td>
<td>3</td>
</tr>
<tr>
<td>2 – 3</td>
<td>2</td>
</tr>
<tr>
<td>4 – 9</td>
<td>1</td>
</tr>
<tr>
<td>More than 10</td>
<td>0</td>
</tr>
</tbody>
</table>

Table 3. Effective power level of combination tone

![Figure 2. Dial tone (a) Time domain (b) Spectrum](image1)

![Figure 3. ETSI DTAS (Dual Tone Alerting Signal) Tone (a) time domain and (b) spectrum](image2)

**5 CONCLUSION**

A simple and generic tone generator design is discussed in detail and performance evaluation tests confirm the high accuracy of the design. The implementation on ARM9E core confirms low power consumption of the tone generator designed. All the features of tone generator are tested and found to be in compliance with the respective tone’s ITU-T guidelines. The generic tone generator may be configured easily for any kind of tone generation in the telephony frequency band, due to the versatility and simplicity of the digital oscillator.

**6 REFERENCES**

[5] ETS 300 001 (1996): "Attachments to the Public Switched Telephone Network (PSTN); General technical requirements for equipment connected to an analogue subscriber interface in the PSTN”.

Published by Research Publications, Chikhli, India